

WEST Search History

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DATE: Friday, October 27, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB,USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L151	L150 and (unlock\$3 near5 structure)	0
<input type="checkbox"/>	L150	L149 and storage and record\$1 and data	21
<input type="checkbox"/>	L149	L148 and (second near5 pointer\$1) and cell\$1	21
<input type="checkbox"/>	L148	L147 and copy\$3 and updat\$3 and version\$1	224
<input type="checkbox"/>	L147	L146 and (control near5 structure)	549
<input type="checkbox"/>	L146	(memory and (first near5 structure) and (second near5 structure) and allocat\$3 and space and access\$3) and @py<=2003	1624
<input type="checkbox"/>	L145	L144 and updat\$3	4
<input type="checkbox"/>	L144	L143 and pointer\$1	5
<input type="checkbox"/>	L143	L142 and (memory near5 space)	5
<input type="checkbox"/>	L142	5870764 .uref.	23
<input type="checkbox"/>	L141	L140 and @py<=2003	2
<input type="checkbox"/>	L140	L139 and l104	2
<input type="checkbox"/>	L139	L138 and (copy\$3 near5 structure)	5
<input type="checkbox"/>	L138	L137 and (file near5 version\$1)	61
<input type="checkbox"/>	L137	L136 and (memory and (third near5 structure))	7914
<input type="checkbox"/>	L136	L135 and (memory and (second near5 structure))	24730
<input type="checkbox"/>	L135	memory and (first near5 structure) and @py<=2003	46042
<input type="checkbox"/>	L134	L133 and (available near5 space)	0
<input type="checkbox"/>	L133	L132 and copy\$3	6
<input type="checkbox"/>	L132	L130 and pointer\$1	11
<input type="checkbox"/>	L131	L130 and (new near5 version\$1)	0
<input type="checkbox"/>	L130	6115716 .uref.	15
<input type="checkbox"/>	L129	L128 and (updat\$3 near5 structure)	9
<input type="checkbox"/>	L128	L127 and l104	52
<input type="checkbox"/>	L127	(memory and structure\$1 and pointer\$1).ti,ab. and @py<=2003	966
<input type="checkbox"/>	L126	6094694 .uref.	0
<input type="checkbox"/>	L125	5951657 .uref.	3
<input type="checkbox"/>	L124	L123 and (second near5 pointer\$1)	1
<input type="checkbox"/>	L123	L122 and (memory near5 cop\$3)	14

□	L122 L121 and (control near5 structure)	48
□	L121 l106 and (memory near5 structure\$1) and (new near5 version\$1) and @py<=2003	196
□	L120 6571326 .uref.	4
□	L119 L118 and pointer\$1	5
□	L118 L117 and updat\$3	5
□	L117 L116 and copy\$3	5
□	L116 L115 and version\$1	5
□	L115 L114 and (memory near5 structure)	14
□	L114 5682497 .uref.	25
□	L113 L112 and (updat\$3 near5 structure)	7
□	L112 l106 and (memory near5 expans\$5)	345
□	L111 L110 and (copy\$3 near5 version\$1)	1
□	L110 L109 and version\$1	26
□	L109 L108 and (pointer\$1 near5 map\$3) and @py<=2003	47
□	L108 L107 and (memory near5 space)	1410
□	L107 L106 and (memory near5 structure\$1)	4471
□	L106 L105 or l104	26478
□	L105 711/147-148,152,161-162,170-171.ccls.	6515
□	L104 707/1,10,100-101,200,203,205.ccls.	20376
□	L103 L102 and (pointer\$1 near5 space)	1
□	L102 L101 and ((first near5 second) same structure\$1)	39
□	L101 L100 and version\$1	97
□	L100 L99 and (control near5 file\$1)	107
□	L99 (free near5 space) and file\$1 and pointer\$1 and index and (bit near5 map\$3) and (data near5 structure) and @py<=2003	254
□	L98 L97 and (new near5 version\$1) and copies and pointer\$1	12
□	L97 L96 and (control near5 structure) and (data near5 file\$1)	128
□	L96 (bit near5 map\$3) and (table near5 space) and (data near5 file\$1) and (data near5 structure) and memory and pointer\$1 and version\$1 and copy\$3 and updat\$3 and @py<=2003	213
□	L95 L94 and copy and index and table\$1 and location	17
□	L94 L92 and ((bit map) near5 memory)	18
□	L93 L92 and ((bit map) near5 record\$1)	0
□	L92 L91 and (index\$3 near5 table\$1)	74
□	L91 L90 and (file\$1 near5 pointer\$1)	165
□	L90 (data near5 base\$1) and (data near5 structure\$1) and (bit near5 map\$3) and (control near5 file\$1) and @py<=2003	612
□	L89 L88 and (version\$1 same pointer\$1)	2

<input type="checkbox"/>	L88	L87 and ((data near5 table\$1) same (table near5 space))	78
<input type="checkbox"/>	L87	L86 and ((data near5 structure) same pointer\$1)	1963
<input type="checkbox"/>	L86	(database\$1 and bit and mapp\$3)	23328
<input type="checkbox"/>	L85	L83 and bit maps	16
<input type="checkbox"/>	L84	L83 and (multiple near5 pointer\$1)	0
<input type="checkbox"/>	L83	L82 and (table near5 space)	64
<input type="checkbox"/>	L82	L81 and (file\$1 same record\$1)	250
<input type="checkbox"/>	L81	L80 and (source near5 target)	652
<input type="checkbox"/>	L80	L79 and (data near5 block\$1)	4816
<input type="checkbox"/>	L79	pointer\$1 and bit and mapp\$3 and version\$1 and (data near5 structures)	7932
<input type="checkbox"/>	L78	L77 and bit and mapp\$3	0
<input type="checkbox"/>	L77	L76 and version\$1	1
<input type="checkbox"/>	L76	6549901.pn.	2
<input type="checkbox"/>	L75	L74 and (table near5 space)	4
<input type="checkbox"/>	L74	L73 and partition\$3	14
<input type="checkbox"/>	L73	L72 and (data near5 record\$1)	21
<input type="checkbox"/>	L72	L71 and (data near5 structures)	24
<input type="checkbox"/>	L71	L70 and memory	24
<input type="checkbox"/>	L70	(target near5 database\$1) and (source near5 database\$1) and pointer\$1 and bit and mapp\$3 and version\$1 and lock\$3 and @py<=2004	24
<input type="checkbox"/>	L69	(target near5 database\$1) and (source near5 database\$1) and pointer\$1 and bit and mapp\$3 and version\$1 and lock\$3 and @py<=2004	0
<input type="checkbox"/>	L68	L67 and (data near5 table\$1) and (table near5 space)	1913
<input type="checkbox"/>	L67	database\$1 and (data near5 structure\$1)	73806
<input type="checkbox"/>	L66	L65 and unlock\$3	12
<input type="checkbox"/>	L65	L64 and lock\$3	22
<input type="checkbox"/>	L64	L62 and (bit near5 mapp\$3)	22
<input type="checkbox"/>	L63	L62 and (bit near5 mapp\$3) or bitmap	22641
<input type="checkbox"/>	L62	L61 and (data near5 structures)	38
<input type="checkbox"/>	L61	L60 and (target same source)	38
<input type="checkbox"/>	L60	L59 and (data near5 structure\$1)	223
<input type="checkbox"/>	L59	L58 and pointers	231
<input type="checkbox"/>	L58	(file near5 header) and (index near5 table\$1) and (bit near5 map)	253
<input type="checkbox"/>	L57	(file near5 header) and (index near5 table\$1) and (bit near5 map) and pointer\$1 and (density near5 map) and (file near5 structure) and updat\$3 and copy\$3 and unlock\$3 and lock\$3 and (new near5 record\$1) and (table near5 space)	0
<input type="checkbox"/>	L56	L55 and ((table near5 space) same (shar\$3 near5 space))	0
<input type="checkbox"/>	L55	L54 and pointer\$1	16
<input type="checkbox"/>	L54	L53 and version\$1	16

<input type="checkbox"/>	L53	L51 and (file\$1 near5 shar\$3) and (lock\$3 or unlock\$3)	16
<input type="checkbox"/>	L52	L51 and (bitmap near5 memory)	0
<input type="checkbox"/>	L51	L50 and (control near5 (file\$1 or record\$1)) and (table near5 space)	29
<input type="checkbox"/>	L50	L49 and ((file\$1 or record\$1) same (bit near5 map))	540
<input type="checkbox"/>	L49	index near5 table\$1	45158
<input type="checkbox"/>	L48	L47 and unlock\$3	0
<input type="checkbox"/>	L47	L46 and lock\$3	5
<input type="checkbox"/>	L46	L45 and version\$1	5
<input type="checkbox"/>	L45	L43 and bit	8
<input type="checkbox"/>	L44	L43 and (bit near5 mapp\$3)	1
<input type="checkbox"/>	L43	L42 and ((table near5 space) same (second near5 structure))	26
<input type="checkbox"/>	L42	(table near5 space) same (first near5 structure)	94
<input type="checkbox"/>	L41	L40 and (bit near5 mapp\$3)	0
<input type="checkbox"/>	L40	L39 and (copy\$3 near5 structure)	12
<input type="checkbox"/>	L39	L38 and (allocat\$3 near5 space)	86
<input type="checkbox"/>	L38	L37 and (table near5 space)	263
<input type="checkbox"/>	L37	L36 and pointer\$1	3974
<input type="checkbox"/>	L36	'data structure'.ti,ab.	21275
<input type="checkbox"/>	L35	L32 and pointer\$1	6
<input type="checkbox"/>	L34	L32 and (bit near5 mapp\$3)	0
<input type="checkbox"/>	L33	L32 and (bit near5 map)	0
<input type="checkbox"/>	L32	(file and structure and database\$1 and space).ti,ab. and @py<=2004	37
<input type="checkbox"/>	L31	(file and structure and database\$1 and space) and @py<=2004	28200
<input type="checkbox"/>	L30	(file near5 structure) and (file near5 lock\$3) and (bit near5 mapp\$3) and (version\$1 or revision\$1) and (first near5 file) and (second near5 file) and (first near5 pointer) and (second near5 pointer) and (first near5 table) and (second near5 table) and @py<=2004	0
<input type="checkbox"/>	L29	(file near5 structure) and (file near5 lock\$3) and (file near5 unlock\$3) and (bit near5 mapp\$3) and (version\$1 or revision\$1) and (first near5 file) and (second near5 file) and (first near5 pointer) and (second near5 pointer) and (first near5 table) and (second near5 table) and @py<=2004	0
<input type="checkbox"/>	L28	L27 and (version\$1 or revision\$1)	7
<input type="checkbox"/>	L27	L26 and ((bit near5 map\$4) same (table near5 space))	7
<input type="checkbox"/>	L26	L25 and ((access\$3 or retriev\$3) same (data or record\$1))	1032
<input type="checkbox"/>	L25	L24 and ((target or second) same (data near5 structure))	1045
<input type="checkbox"/>	L24	L23 and ((source or first) same (data near5 structure))	1856
<input type="checkbox"/>	L23	memory and file\$1 and data and structure and table and space and bit and mapp\$3 and pointer\$1 and version\$1 and @py<=2004	5485
<input type="checkbox"/>	L22	L21 and (second near5 (record or file)) and (second near5 pointer)	7
<input type="checkbox"/>	L21	L20 and (first near5 (record or file)) and (first near5 pointer)	31

<input type="checkbox"/>	L20	database\$1 same (table near5 space)	941
<input type="checkbox"/>	L19	(file and management and structure and pointers and bit and map and (usable near5 space) and version\$1 and copy\$3 and updat\$3) and @py<=2004	20
<input type="checkbox"/>	L18	L17 and (table near5 space)	4
<input type="checkbox"/>	L17	L14 and (first near5 file) and (second near5 file) and (first near5 pointer) and (second near5 pointer)	69
<input type="checkbox"/>	L16	(first near5 file) and (second near5 file) and (first near5 pointer) and (second near5 pointer)	0
<input type="checkbox"/>	L15	L14 and (first near5 file) and (second near5 file) and (first near5 pointer) and (second near5 pointer)	0
<input type="checkbox"/>	L14	plurality same 'data structures'	14694
<input type="checkbox"/>	L13	'plurality of data structures'	0
<input type="checkbox"/>	L12	'plurality of data structures'	0
<input type="checkbox"/>	L11	L10 and version\$1 and copy\$3 and updat\$3	4
<input type="checkbox"/>	L10	L8 and (bit near5 mapp\$3) and pointer\$1	13
<input type="checkbox"/>	L9	L8 and ((bit near5 mapp\$3) same (pointer\$1))	0
<input type="checkbox"/>	L8	L7 and (plurality near10 (data structures))	146
<input type="checkbox"/>	L7	(database\$1 or data\$base\$1) and (table near5 space) and (data near5 structure) and @py<=2004	1635
<input type="checkbox"/>	L6	L5 and (bit near5 map)	8
<input type="checkbox"/>	L5	L4 and ((data near5 file\$1) or (data near5 record\$1))	56
<input type="checkbox"/>	L4	L3 and (first near5 pointer) and (second near5 pointer)	96
<input type="checkbox"/>	L3	L2 and database\$1	1069
<input type="checkbox"/>	L2	L1 and (plurality near5 (data structure))	2088
<input type="checkbox"/>	L1	first near5 (data structure)	9896

END OF SEARCH HISTORY

WEST Search History

DATE: Friday, October 27, 2006

Hide?	<u>Set</u> <u>Name</u>	<u>Query</u>	<u>Hit</u> <u>Count</u>
	<i>DB=PGPB; PLUR=YES; OP=ADJ</i>		
<input type="checkbox"/>	L36	(memory and bit and map\$3 and pointer\$1 and cell\$1 and divid\$3 and data and structure).clm.	0
<input type="checkbox"/>	L35	(memory and bit and map\$3 and pointer\$1 and cell\$1 and divid\$3 and data and version\$1).clm.	0
<input type="checkbox"/>	L34	(memory and bit and map\$3 and pointer\$1 and cell\$1 and divid\$3 and data).clm.	1
<input type="checkbox"/>	L33	(memory and bit and map\$3 and pointer\$1 and cell\$1 and divid\$3).clm.	1
<input type="checkbox"/>	L32	(memory and bit and map\$3 and pointer\$1 and cell\$1).clm.	6
<input type="checkbox"/>	L31	(memory and updat\$3 and copy\$3 and data and file\$1 and pointer\$1 and lock\$3 and unlock\$3 and record\$1 and storage and quantities).clm.	0
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<input type="checkbox"/>	L29	(memory and updat\$3 and copy\$3 and data and file\$1 and pointer\$1 and lock\$3 and unlock\$3 and record\$1).clm.	2
<input type="checkbox"/>	L28	(memory and updat\$3 and copy\$3 and data and file\$1 and pointer\$1 and lock\$3 and unlock\$3).clm.	2
<input type="checkbox"/>	L27	(memory and updat\$3 and copy\$3 and data and file\$1 and pointer\$1 and lock\$3).clm.	3
<input type="checkbox"/>	L26	(memory and updat\$3 and copy\$3 and data and file\$1 and pointer\$1).clm.	27
<input type="checkbox"/>	L25	(memory and lock\$3 and unlock\$3 and structure\$1 and pointer\$1 and message and shar\$3).clm.	0
<input type="checkbox"/>	L24	(memory and lock\$3 and unlock\$3 and structure\$1 and pointer\$1 and message).clm.	1
<input type="checkbox"/>	L23	(memory and lock\$3 and unlock\$3 and structure\$1 and pointer\$1).clm.	6
<input type="checkbox"/>	L22	(memory and lock\$3 and unlock\$3 and structure\$1).clm.	57
<input type="checkbox"/>	L21	(memory and allocat\$3 and data and structure and pointer\$1 and version\$1 and shar\$3 and application\$1).clm.	0
<input type="checkbox"/>	L20	(memory and allocat\$3 and data and structure and pointer\$1 and version\$1 and shar\$3).clm.	2
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<input type="checkbox"/>	L17	(memory and second and structure and space and data and lock\$3 and unlock\$3).clm.	2
	L16	(memory and second and structure and space and data and lock\$3 and	2

<input type="checkbox"/>		version\$1).clm.	
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<input type="checkbox"/>	L14	(memory and second and structure and space and data and lock\$3 and file\$1).clm.	43
<input type="checkbox"/>	L13	(memory and second and structure and space and data and lock\$3).clm.	66
<input type="checkbox"/>	L12	(memory and second and structure and space and data).clm.	580
<input type="checkbox"/>	L11	(memory and space and application and data and file\$1 and structure\$1 and access\$3 and copy\$3 and pointer\$1 and version\$1 and cell\$1).clm.	0
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<input type="checkbox"/>	L9	(memory and space and application and data and file\$1 and structure\$1 and access\$3 and copy\$3 and pointer\$1).clm.	3
<input type="checkbox"/>	L8	(memory and space and application and data and file\$1 and structure\$1 and access\$3 and copy\$3) and pointer\$1.clm.	1022
<input type="checkbox"/>	L7	(memory and space and application and data and file\$1 and structure\$1 and access\$3 and copy\$3).clm.	14
<input type="checkbox"/>	L6	(memory and space and application and data and file\$1 and structure\$1 and access\$3 and expand\$3 and allocat\$3 and copy\$3).clm.	0
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<input type="checkbox"/>	L4	(memory and space and application and data and file\$1 and structure\$1 and access\$3 and expand\$3).clm.	35
<input type="checkbox"/>	L3	(memory and space and application and data and file\$1 and structure\$1 and access\$3).clm.	98
<input type="checkbox"/>	L2	(memory and space and application and data and file\$1 and structure).clm.	115
<input type="checkbox"/>	L1	(memory and space and application and data and file\$1).clm.	270

END OF SEARCH HISTORY

Day : Friday
Date: 10/27/2006

Time: 15:33:44

 **PALM INTRANET**

Inventor Information for 10/607885

Inventor Name	City	State/Country
TITUS, SCOTT L.	SHAKOPEE	MINNESOTA
CRANDALL, SHERRY L.	HUGO	MINNESOTA

Appln Info	Contents	Petition Info	Atty/Agent Info	Continuity/Reexam	Foreign I
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Attorney Docket #

Bar Code #

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10/607,885

-STIC/EIC search.

Set	Items	Description
S1	787347	SPACE OR FREESPACE OR ROOM
S2	1478399	QUANTITY OR QUANTITIES OR AMOUNT OR NUMBER
S3	1313872	FILE? ? OR DATASET? ? OR DATA()SET? ? OR STORAGE OR MEMORY OR DATAFILE? ?
S4	64961	(AVAILABLE OR ATTAINABLE OR AVAILIABLE OR AVAILIABLE OR AVAILIABLE OR FREE OR OBTAINABLE OR (ON(2W)HAND) OR OPEN OR PROCURABLE OR READY OR REAL OR SUPPLIABLE OR UNCLAIMED OR UNOCCUPIED OR DEALLOCAT??? OR PUBLIC) (3N)S1
S5	12033	(ASSIGN??? OR ASSIGNMENT OR ALLOCAT??? OR ALLOT? ? OR ALLOTMENT? ? OR REBALANC??? OR REALLOCAT??? OR APPORTION? OR REAPPORTION? OR SCHEDULING OR RESERV??? OR RESERVATION? ? OR BOOK??? OR BOOKING? ? OR REDISTRIBUT??? (3N)S1
S6	7934	(USABLE OR ACCESSIBLE OR UNUSED OR UTILIZABLE OR ACCEPTABLE OR VALID OR APPROVED OR USEFUL OR REUSABLE) (3N)S1
S7	3259	S2 (3N)S4
S8	1655331	POINTER? ? OR LOCAT??? OR OFFSET???? OR INDEX??? OR IDENTIFIER? ? OR MARKER? ? OR REFERENCE? ? OR INDICATOR? ? OR BITMAP???? OR BITSTREAM??? OR BIT() (MAP???? OR STREAM???)
S9	10	(S8(3N)S4) AND (S8(3N)S5) AND (S8(3N)S6)
S10	6215	(TWO OR DUAL OR PAIR OR TWIN OR SECOND??? OR OTHER OR ANOTHER OR 2ND OR NEW OR PRESENT OR SEPARATE OR ADDITIONAL OR EXTRA OR DIFFERENT OR ALTERNAT????? OR MORE(W)THAN(W)ONE OR DIFFERING OR FRESH OR SUBSEQUENT OR SUCCESSIVE OR CURRENT OR BOTH OR TWICE OR
S11	3356	(NEW OR FRESH OR RECENT?? OR REDEFINED OR REENGINEERED OR REPLACEMENT OR REPLAC??? OR TWO OR DUAL OR PAIR OR TWIN OR SECOND??? OR OTHER OR ANOTHER OR 2ND OR NEW OR PRESENT OR SEPARATE OR ADDITIONAL OR EXTRA OR DIFFERENT OR ALTERNAT????? OR MORE(W)THAN(W)
S12	362	(BUILD??? OR CREAT??? OR CONSTRUCT??? OR MAKE OR MAKING OR MADE OR GENERAT??? OR PRODUC??? OR RECONSTRUCT??? OR REBUILD??? (5N)S11
S13	12259	(EXPAND??? OR EXPANS???? OR ENLARG??? OR UNCOMPRESS? OR ADJUST???? OR READJUST????) (3N)S3
S14	292	(ACCOMMODAT??? OR CONFORM??? OR ADAPT????? OR COMPLY??? OR FIT OR FITTING OR MODIF??????? OR TAILOR???) (5N)S13
S15	0	(INSUFFICIENT OR (TOO()SMALL) OR ("NOT"() (LARGE OR BIG) ()ENOUGH) OR INADEQUA????) (10N)S14

? show files

File 348:EUROPEAN PATENTS 1978-2006/ 200643

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File 349:PCT FULLTEXT 1979-2006/UB=20061026UT=20061019

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?

1584973 TWO
 133884 DUAL
 468402 PAIR
 33918 TWIN
 1500963 SECOND???
 1716644 OTHER
 1371947 ANOTHER
 61832 2ND
 811136 NEW
 1585407 PRESENT
 1042636 SEPARATE
 819895 ADDITIONAL
 130263 EXTRA
 1214117 DIFFERENT
 1169981 ALTERNAT??????
 1596254 MORE
 1512329 THAN
 2025725 ONE
 230958 MORE (W) THAN (W) ONE
 98910 DIFFERING
 116316 FRESH
 467889 SUBSEQUENT
 153001 SUCCESSIVE
 596552 CURRENT
 1311780 BOTH
 202800 TWICE
 548207 COUPLE? ?
 1063393 CONTROL
 124624 MANAG????
 1004940 STRUCTURE? ?
 646854 BLOCK? ?
 3024 ((((((((((TWO OR DUAL) OR PAIR) OR TWIN) OR SECOND???)
 OR OTHER) OR ANOTHER) OR 2ND) OR NEW) OR PRESENT) OR
 SEPARATE) OR ADDITIONAL)..... 8 c c
 c c
 C...
 3259 S7
 S10 6215 (TWO OR DUAL OR PAIR OR TWIN OR SECOND???) OR OTHER OR
 ANOTHER OR 2ND OR NEW OR PRESENT OR SEPARATE OR
 ADDITIONAL OR EXTRA OR DIFFERENT OR ALTERNAT?????? OR
 MORE (W) THAN (W) ONE OR DIFFERING OR FRESH OR SUBSEQUENT OR
 SUCCESSIVE OR CURRENT OR BOTH OR TWICE OR COUPLE?
 ?) (3N) ((CONTROL OR MANAG????) () (STRUCTURE? ? OR BLOCK?
 ?)) OR S7

811136 NEW

116316 FRESH
351294 RECENT??
2704 REDEFINED
230 REENGINEERED
150015 REPLACEMENT
503835 REPLAC???
1584973 TWO
133884 DUAL
468402 PAIR
33918 TWIN
1500963 SECOND???
1716644 OTHER
1371947 ANOTHER
61832 2ND
811136 NEW
1585407 PRESENT
1042636 SEPARATE
819895 ADDITIONAL
130263 EXTRA
1214117 DIFFERENT
1169981 ALTERNAT??????
1596254 MORE
1512329 THAN
2025725 ONE
230958 MORE (W) THAN (W) ONE
98910 DIFFERING
116316 FRESH
467889 SUBSEQUENT
153001 SUCCESSIVE
596552 CURRENT
1311780 BOTH
202800 TWICE
548207 COUPLE? ?
6215 S10

S11 3356 (NEW OR FRESH OR RECENT?? OR REDEFINED OR REENGINEERED OR
REPLACEMENT OR REPLAC??? OR TWO OR DUAL OR PAIR OR TWIN
OR SECOND?? OR OTHER OR ANOTHER OR 2ND OR NEW OR PRESENT
OR SEPARATE OR ADDITIONAL OR EXTRA OR DIFFERENT OR
ALTERNAT????? OR MORE (W) THAN (W) ONE OR DIFFERING OR FRESH
OR SUBSEQUENT OR SUCCESSIVE OR CURRENT OR BOTH OR TWICE
OR COUPLE? ?) (3N) S10

Set	Items	Description
S1	5944665	SPACE OR FREESPACE OR ROOM
S2	13971991	QUANTITY OR QUANTITIES OR AMOUNT OR NUMBER
S3	8013523	FILE? ? OR DATASET? ? OR DATA()SET? ? OR STORAGE OR MEMORY OR DATAFILE? ?
S4	350726	(AVAILABLE OR ATTAINABLE OR AVAILIABLE OR AVAILIABLE OR AVAILIABLE OR FREE OR OBTAINABLE OR (ON(2W)HAND) OR OPEN OR PROCURABLE OR READY OR REAL OR SUPPLIABLE OR UNCLAIMED OR UNOCCUPIED OR DEALLOCAT??? OR PUBLIC) (3N)S1
S5	91041	(ASSIGN??? OR ASSIGNMENT OR ALLOCAT??? OR ALLOT? ? OR ALLOTMENT? ? OR REBALANC??? OR REALLOCAT??? OR APPORTION? OR REAPPORTION? OR SCHEDULING OR RESERV??? OR RESERVATION? ? OR BOOKING? ? OR REDISTRIBUT??? (3N)S1
S6	26057	(USABLE OR ACCESSIBLE OR UNUSED OR UTILIZABLE OR ACCEPTABLE OR VALID OR APPROVED OR USEFUL OR REUSABLE) (3N)S1
S7	7047	S2(3N)S4
S8	11046777	POINTER? ? OR LOCAT??? OR OFFSET???? OR INDEX??? OR IDENTIFIER? ? OR MARKER? ? OR REFERENCE? ? OR INDICATOR? ? OR BITMAP???? OR BITSTREAM??? OR BIT() (MAP???? OR STREAM???)
S9	0	(S8(3N)S4) AND (S8(3N)S5) AND (S8(3N)S6)
S10	8547	(TWO OR DUAL OR PAIR OR TWIN OR SECOND??? OR OTHER OR ANOTHER OR 2ND OR NEW OR PRESENT OR SEPARATE OR ADDITIONAL OR EXTRA OR DIFFERENT OR ALTERNAT????? OR MORE(W)THAN(W)ONE OR DIFFERING OR FRESH OR SUBSEQUENT OR SUCCESSIVE OR CURRENT OR BOTH OR TWICE OR
S11	2091	(NEW OR FRESH OR RECENT?? OR REDEFINED OR REENGINEERED OR REPLACEMENT OR REPLAC??? OR TWO OR DUAL OR PAIR OR TWIN OR SECOND??? OR OTHER OR ANOTHER OR 2ND OR NEW OR PRESENT OR SEPARATE OR ADDITIONAL OR EXTRA OR DIFFERENT OR ALTERNAT????? OR MORE(W)THAN(W)
S12	266	(BUILD??? OR CREAT??? OR CONSTRUCT??? OR MAKE OR MAKING OR MADE OR GENERAT??? OR PRODUC??? OR RECONSTRUCT??? OR REBUILD??? (5N)S11
S13	98317	(EXPAND??? OR EXPANS???? OR ENLARG??? OR UNCOMPRESS? OR ADJUST???? OR READJUST????) (3N)S3
S14	2313	(ACCOMMODAT??? OR CONFORM??? OR ADAPT????? OR COMPLY??? OR FIT OR FITTING OR MODIF??????? OR TAILOR???) (5N)S13
S15	2	(INSUFFICIENT OR (TOO())SMALL) OR ("NOT"()) (LARGE OR BIG) (ENOUGH) OR INADEQUA????) (3N)S14
S16	0	S12(5N)S15
S17	0	S12 AND S15

? show files

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File 160:Gale Group PROMT(R) 1972-1989
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(c) 2006 ProQuest Info&Learning
File 9:Business & Industry(R) Jul/1994-2006/Oct 27
(c) 2006 The Gale Group
File 13:BAMP 2006/Oct W4
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File 810:Business Wire 1986-1999/Feb 28
(c) 1999 Business Wire
File 610:Business Wire 1999-2006/Oct 28
(c) 2006 Business Wire.
File 647:CMP Computer Fulltext 1988-2006/Dec W2
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(c)2006 The Gale Group
File 634:San Jose Mercury Jun 1985-2006/Oct 25
(c) 2006 San Jose Mercury News
File 636:Gale Group Newsletter DB(TM) 1987-2006/Oct 27
(c) 2006 The Gale Group

?

18227810 TWO
829878 DUAL
625489 PAIR
361648 TWIN
9527566 SECOND???
26751266 OTHER
8007941 ANOTHER
552755 2ND
32854371 NEW
3610285 PRESENT
2308860 SEPARATE
7765388 ADDITIONAL
1420701 EXTRA
6045045 DIFFERENT
3243191 ALTERNAT??????
27853100 MORE
22416198 THAN
23423714 ONE
599792 MORE (W) THAN (W) ONE
185829 DIFFERING
1139690 FRESH
1073443 SUBSEQUENT
166106 SUCCESSIVE
8563314 CURRENT
13775938 BOTH
1073792 TWICE
2129700 COUPLE? ?
7203526 CONTROL
12950920 MANAG????
4049616 STRUCTURE? ?
1799741 BLOCK? ?
1504 ((((((((((TWO OR DUAL) OR PAIR) OR TWIN) OR SECOND???)
OR OTHER) OR ANOTHER) OR 2ND) OR NEW) OR PRESENT) OR
SEPARATE) OR ADDITIONAL)..... 8 c c c c

C...
7047 S7
S10 8547 (TWO OR DUAL OR PAIR OR TWIN OR SECOND???) OR OTHER OR
ANOTHER OR 2ND OR NEW OR PRESENT OR SEPARATE OR
ADDITIONAL OR EXTRA OR DIFFERENT OR ALTERNAT?????? OR
MORE (W) THAN (W) ONE OR DIFFERING OR FRESH OR SUBSEQUENT OR
SUCCESSIVE OR CURRENT OR BOTH OR TWICE OR COUPLE?
?) (3N) ((CONTROL OR MANAG????) () (STRUCTURE? ? OR BLOCK?
?)) OR S7

32854371 NEW

1139690 FRESH
11540052 RECENT??
59648 REDEFINED
12010 REENGINEERED
931884 REPLACEMENT
2781687 REPLAC???
18227810 TWO
829878 DUAL
625489 PAIR
361648 TWIN
9527566 SECOND???
26751266 OTHER
8007941 ANOTHER
552755 2ND
32854371 NEW
3610285 PRESENT
2308860 SEPARATE
7765388 ADDITIONAL
1420701 EXTRA
6045045 DIFFERENT
3243191 ALTERNAT??????
27853100 MORE
22416198 THAN
23423714 ONE
599792 MORE (W) THAN (W) ONE
185829 DIFFERING
1139690 FRESH
1073443 SUBSEQUENT
166106 SUCCESSIVE
8563314 CURRENT
13775938 BOTH
1073792 TWICE
2129700 COUPLE? ?
8547 S10

S11 2091 (NEW OR FRESH OR RECENT?? OR REDEFINED OR REENGINEERED OR
REPLACEMENT OR REPLAC??? OR TWO OR DUAL OR PAIR OR TWIN
OR SECOND?? OR OTHER OR ANOTHER OR 2ND OR NEW OR PRESENT
OR SEPARATE OR ADDITIONAL OR EXTRA OR DIFFERENT OR
ALTERNAT?????? OR MORE (W) THAN (W) ONE OR DIFFERING OR FRESH
OR SUBSEQUENT OR SUCCESSIVE OR CURRENT OR BOTH OR TWICE
OR COUPLE? ?) (3N) S10

Set	Items	Description
S1	0	AU=((TITUS S? OR TITUS, S?) AND (CRANDALL S? OR CRANDALL, S?))
S2	29	AU=(TITUS S? OR TITUS, S? OR CRANDALL S? OR CRANDALL, S?) - AND (FILE? ? OR DATASET? ? OR DATA()SET? ? OR STORAGE OR MEMORY OR DATAFILE? ?)
S3	23	RD (unique items)
? show files		
File	2:INSPEC 1898-2006/Oct W3	(c) 2006 Institution of Electrical Engineers
File	6:NTIS 1964-2006/Oct W3	(c) 2006 NTIS, Intl Cpyrght All Rights Res
File	8:Ei Compendex(R) 1970-2006/Oct W3	(c) 2006 Elsevier Eng. Info. Inc.
File	34:SciSearch(R) Cited Ref Sci 1990-2006/Oct W4	(c) 2006 The Thomson Corp
File	65:Inside Conferences 1993-2006/Oct 27	(c) 2006 BLDSC all rts. reserv.
File	94:JICST-EPlus 1985-2006/Jul W3	(c)2006 Japan Science and Tech Corp(JST)
File	99:Wilson Appl. Sci & Tech Abs 1983-2006/Sep	(c) 2006 The HW Wilson Co.
File	148:Gale Group Trade & Industry DB 1976-2006/Oct 30	(c)2006 The Gale Group
File	636:Gale Group Newsletter DB(TM) 1987-2006/Oct 27	(c) 2006 The Gale Group

Set	Items	Description
S1	0	AU=((TITUS, S? OR TITUS S?) AND (CRANDALL, S? OR CRANDALL - S?))
S2	9	AU=(TITUS, S? OR TITUS S? OR CRANDALL, S? OR CRANDALL S?) - AND (FILE? ? OR DATASET? ? OR DATA()SET? ? OR STORAGE OR MEMORY OR DATAFILE? ?)

? show files

File 347:JAPIO Dec 1976-2006/Jan(Updated 061009)

(c) 2006 JPO & JAPIO

File 350:Derwent WPIX 1963-2006/UD=200669

(c) 2006 The Thomson Corporation

RESULT LIST

Approximately **11895** results found in the Worldwide database for:
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Only the first **500** results are displayed.
(Results are sorted by date of upload in database)

- 1 Apparatus for emulating asynchronous clear in memory structure and method for implementing the same**
Inventor: YUAN JINYONG (US); KAZARIAN PETER (US) Applicant: ALTERA CORP (US)
EC: IPC: **G11C7/10; G11C7/10**
Publication info: **US7126858** - 2006-10-24
- 2 Interlocking memory/logic cell layout and method of manufacture**
Inventor: BANACHOWICZ BARTOSZ (US); WRIGHT ANDREW (US) Applicant: NETLOGIC MICROSYSTEMS INC (US)
EC: IPC: **G11C5/06; G11C5/06**
Publication info: **US7126837** - 2006-10-24
- 3 SPLIT GATE FLASH MEMORY CELL AND MANUFACTURING METHOD THEREOF**
Inventor: WU SHENG (TW); HUANG TSAI-YU (TW) Applicant:
EC: IPC: **H01L21/336; H01L21/02**
Publication info: **US2006234444** - 2006-10-19
- 4 NON-VOLATILE MEMORY AND FABRICATING METHOD THEREOF**
Inventor: LEE TZYH-CHEANG (TW) Applicant:
EC: IPC: **H01L21/336; H01L21/02**
Publication info: **US2006234452** - 2006-10-19
- 5 Structure for amorphous carbon based non-volatile memory**
Inventor: MOORE JOHN (US); CAMPBELL KRISTY A (US); (+1) Applicant: MICRON TECHNOLOGY INC
EC: IPC: **H01L29/02; H01L29/02**
Publication info: **US2006231823** - 2006-10-19
- 6 Novel monolithic, combo nonvolatile memory allowing byte, page and block write with no disturb and divided-well in the cell array using a unified cell structure and technology with a new scheme of decoder and layout**
Inventor: LEE PETER W (US); HSU FU-CHANG (US); (+3) Applicant: APLUS FLASH TECHNOLOGY INC
EC: IPC: **H01L21/00; H01L21/00**
Publication info: **US2006234394** - 2006-10-19
- 7 Method for fabricating a nonvolatile memory element and a nonvolatile memory element**
Inventor: UFERT KLAUS-DIETER (DE) Applicant:
EC: IPC: **H01L51/40; H01L51/05**
Publication info: **US2006234418** - 2006-10-19
- 8 Memory device having serially connected resistance nodes**
Inventor: KIM WON-JOO (KR); PARK YOON-DONG (KR) Applicant: SAMSUNG ELECTRONICS CO LTD
EC: IPC: **H01L29/788; H01L29/66**
Publication info: **US2006231887** - 2006-10-19
- 9 RDMA enabled I/O adapter performing efficient memory management**
Inventor: HAUSAUER BRIAN S (US); SHARP ROBERT O (US) Applicant: NETEFFECT INC (US)
EC: IPC: **G06F12/00; G06F12/00**
Publication info: **US2006236063** - 2006-10-19

10 Redundancy circuits for semiconductor memory

Inventor: OH JONG-HOON (US)

Applicant:

EC:

IPC: *G11C11/00*; *G11C11/00*

Publication info: **US2006233014** - 2006-10-19

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RESULT LIST

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memory in the title AND **pointers** in the title or abstract
(Results are sorted by date of upload in database)

11 A MEMORY STRUCTURE

Inventor: SERROYEN GERT (BE); VANCOMPERNOLLE HANS (BE); (+2)
EC: G06F17/30P1D
Applicant: SIEMENS AG (DE); SERROYEN GERT (BE); (+3)
IPC: G06F17/30; G06F17/30; (IPC1-7): G06F17/30
Publication info: WO2006005623 - 2006-01-19

12 Compiling memory dereferencing instructions from software to hardware in an electronic design

Inventor: MOONA RAJAT (IN); KLEIN RUSSELL A (US)
EC:
Applicant: MENTOR GRAPHICS CORP
IPC: G06F17/50; G06F17/50
Publication info: US2006031791 - 2006-02-09

13 Efficient multiple priority list memory system

Inventor: MIDDLETON JOSEPH (US); BOKAIE DAVID (US)
EC:
Applicant: CISCO TECH IND (US)
IPC: G06F9/00; G06F9/00
Publication info: US6996820 - 2006-02-07

14 Multiple counters to relieve flag restriction in a multi-queue first-in first-out memory system

Inventor: AU MARIO (US); MO JASON Z (US)
EC:
Applicant: INTEGRATED DEVICE TECH (US)
IPC: G11C8/00; G11C8/00
Publication info: US2006018177 - 2006-01-26

15 Mark/re-read and mark/re-write operations in a multi-queue first-in first-out memory system

Inventor: AU MARIO (US); MO JASON Z (US)
EC:
Applicant: INTEGRATED DEVICE TECH (US)
IPC: G11C8/00; G11C8/00
Publication info: US2006018176 - 2006-01-26

16 SCALABLE DISTRIBUTED PARALLEL ACCESS MEMORY SYSTEMS WITH INTERNET ROUTING APPLICATIONS

Inventor: REED COKE S (US); MURPHY DAVID (US)
EC:
Applicant: INTERACTIC HOLDINGS LLC (US); REED COKE S (US); (+1)
IPC: (IPC1-7): G06F15/173; H04L12/28; H04L12/56
Publication info: WO2005124577 - 2005-12-29

17 Computer for safe technical arrangement of transport system with rail-bound train, has information area including logical blocks, each with validity note, where computer allows execution of program in volatile memory

Inventor: SCHWEISTHAL HANS-HELMUT (DE)
EC:
Applicant: SIEMENS AG (DE)
IPC: G06F11/08; G06F11/08
Publication info: DE102004029614 - 2006-01-05

18 Memory, method, and program product for organizing data using a compressed trie table

Inventor: SEWARD ROBERT Y (US)
EC:
Applicant:
IPC: G06F7/00; G06F7/00; (IPC1-7): G06F7/00
Publication info: US2005256823 - 2005-11-17

19 Switch memory management using a linked list structure

Inventor: SOKOL MICHAEL A (US)
EC:
Applicant: BROADCOM CORP
IPC: G06F7/00; G06F12/00; G06F7/00 (+2)
Publication info: US2005235129 - 2005-10-20

20 Memory control method, storage device, control program, and readable record medium

Inventor: KAY ANDREW (GB); MILLER RICHARD (GB) **Applicant:**

EC: G06F12/02D2E2

IPC: *G06F12/02*; *G06F12/02*; (IPC1-7): G06F7/00

Publication info: **US2005119987** - 2005-06-02

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RESULT LIST

Approximately **11895** results found in the Worldwide database for:
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(Results are sorted by date of upload in database)

11 Memory card structure

Inventor: CHEN CHIEN-YUAN (TW)

Applicant: POWER DIGITAL CARD CO LTD

EC:

IPC: **G06K19/06; G06K19/06**

Publication info: **US2006231634** - 2006-10-19

12 Two-terminal solid-state memory device and two-terminal flexible memory device based on nanocrystals or nanoparticles

Inventor: CHEN TUPEI (SG); LIU YANG (SG); (+2)

Applicant:

EC:

IPC: **H01L29/792; H01L29/66**

Publication info: **US2006231889** - 2006-10-19

13 Memory cell structure

Inventor: WANG YU-JEN (TW); LAI CHIH-HUANG (TW); Applicant: TAIWAN SEMICONDUCTOR MFG (+2)

EC:

IPC: **G11C19/08; G11C19/00**

Publication info: **US2006233002** - 2006-10-19

14 A HIGH SPEED LOW POWER CONSUMPTION PHASE CHANGE MEMORY CELL ARRAY STRUCTURE AND HIGH DENSITY MEMORY DEVICE ARCHITECTURE HAVING THE SAME

Inventor: CHU EU GENE (KR); MO JU HO (KR); (+6)

Applicant: BEYONDMICRO INC (KR)

EC:

IPC: **H01L27/10; H01L27/115; H01L27/10** (+1)

Publication info: **KR20060002672** - 2006-01-09

15 DOOR SIDE WEATHER STRIP STRUCTURE USING SHAPE MEMORY ALLOY IN A VEHICLE

Inventor: KIM KI CHANG (KR)

Applicant: HYUNDAI MOTOR CO LTD (KR)

EC:

IPC: **B60J10/08; B60J10/00**

Publication info: **KR20060000301** - 2006-01-06

16 METHOD FOR FORMING A MEMORY STRUCTURE USING A MODIFIED SURFACE TOPOGRAPHY AND STRUCTURE THEREOF

Inventor: INGERSOLL PAUL A (US); CHINDALORE GOWRISHANKAR L (US); (+1)

Applicant: FREESCALE SEMICONDUCTOR INC (US)

EC: **C09K17/14; C09K17/40**

IPC: **H01L21/336; C09K17/14; C09K17/40** (+11)

Publication info: **EP1714314** - 2006-10-25

17 METHOD FOR MANUFACTURING CONTACT IN SEMICONDUCTOR MEMORY DEVICE HAVING SELF-ALIGNED CONTACT STRUCTURE

Inventor: KWON DONG CHEOL (KR)

Applicant: SAMSUNG ELECTRONICS CO LTD (KR)

EC:

IPC: **H01L27/10; H01L27/10; (IPC1-7): H01L27/10**

Publication info: **KR2001066213** - 2001-07-11

18 METHOD FOR SETTING MEMORY STRUCTURE IN ORDER TO EFFECTIVELY USE INPUT VALUE DEFINITION FILE BY DATABASE FIELDS

Inventor: KIM GEON HO (KR); KIM GI CHEOL (KR); (+2)

Applicant: KOREA TELECOMMUNICATION (KR)

EC:

IPC: **G06F17/30; G06F17/30; (IPC1-7): G06F17/30**

Publication info: **KR2001059625** - 2001-07-06

19 INPUT/OUTPUT STRUCTURE OF SEMICONDUCTOR MEMORY DEVICE

Inventor: LEE BYEONG JAE (KR)

Applicant: HYNIX SEMICONDUCTOR INC (KR)

EC: **G11C7/10P; G11C7/10W; (+2)**

IPC: **G11C7/10; G11C7/18; G11C8/12** (+4)

Publication info: **KR2001059017** - 2001-07-06

**20 CYLINDRICAL CAPACITOR STRUCTURE OF SEMICONDUCTOR
MEMORY DEVICE HAVING TAON ETCHING STOP FILM, AND
MANUFACTURING METHOD THEREOF**

Inventor: PARK DONG SU (KR); PARK GI SEON (KR)

Applicant: HYNIX SEMICONDUCTOR INC (KR)

EC:

IPC: **H01L27/108**; **H01L27/108**; (IPC1-7):
H01L27/108

Publication info: **KR2001058611** - 2001-07-06

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Approximately **11895** results found in the Worldwide database for:
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 Only the first **500** results are displayed.
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- 21 MEMORY DEVICE OF SINGLE PORT STRUCTURE ABLE TO PERFORM READ AND WRITE OPERATION AS TO DATA SIMULTANEOUSLY**
Inventor: LIM SEONG MUK (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: IPC: **G11C11/413; G11C11/413;** (IPC1-7): G11C11/413
Publication info: **KR2001058521** - 2001-07-06
 - 22 FERROELECTRIC MEMORY DEVICE WITH CAPACITOR CONTACT DIFFUSION BARRIER FORMED WITH STACKED STRUCTURE OF IRIIDIUM AND IRIIDIUM OXIDE AND METHOD FOR MANUFACTURING THE SAME**
Inventor: KWON SUN YONG (KR); LEE SANG GYU (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: IPC: **H01L27/105; H01L27/105;** (IPC1-7): H01L27/105
Publication info: **KR2001058494** - 2001-07-06
 - 23 METHOD FOR MANUFACTURING SEMICONDUCTOR MEMORY DEVICE OF DUAL GATE STRUCTURE**
Inventor: OH SE JUNG (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: IPC: **H01L27/10; H01L27/10;** (IPC1-7): H01L27/10
Publication info: **KR2001065174** - 2001-07-11
 - 24 MEMORY REPAIR CIRCUIT USING MOS STRUCTURE ANTI-FUSE**
Inventor: CHO HO YEOP (KR); KIM PIL JUNG (KR); (+4) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: G11C17/18; G11C29/00R8F IPC: **H01L27/04; G11C17/18; G11C29/00** (+11)
Publication info: **KR2001065138** - 2001-07-11
 - 25 MEMORY STRUCTURE CAPABLE OF LOCATING FAILURE OF WAFER USING DUMMY PATTERN AND METHOD OF MANUFACTURING THE MEMORY STRUCTURE**
Inventor: LEE SEONG GWON (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: IPC: **G11C29/00; G11C29/00;** (IPC1-7): G11C29/00
Publication info: **KR2001071101** - 2001-07-28
 - 26 FERROELECTRIC MEMORY DEVICE HAVING BIT LINE STRUCTURE CONSISTING OF DEPLETION TRANSISTOR**
Inventor: KANG EUNG YEOL (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: G11C11/22; H01L27/115F IPC: **G11C11/22; H01L21/8246; H01L27/105** (+6)
Publication info: **KR2001061559** - 2001-07-07
 - 27 REPAIR STRUCTURE OF FLASH MEMORY**
Inventor: JU YEONG DONG (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: IPC: **G11C29/00; G11C29/00;** (IPC1-7): G11C29/00
Publication info: **KR2001061475** - 2001-07-07
 - 28 MEMORY CELL STRUCTURE**
Inventor: YOON HA RYONG (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: IPC: **G11C7/00; G11C7/00;** (IPC1-7): G11C7/00
Publication info: **KR2001061381** - 2001-07-07
 - 29 SEMICONDUCTOR MEMORY DEVICE WITH INPUT/OUTPUT STRUCTURE FOR DISPERSING LOAD OF INPUT/OUTPUT LINE**
Inventor: KIM GANG YONG (KR) Applicant: HYNIX SEMICONDUCTOR INC (KR)
EC: G11C7/10P; G11C7/18 IPC: **G11C7/10; G11C7/18; G11C7/00** (+2)

Publication info: **KR2001061117** - 2001-07-07

30 BIAS STRUCTURE OF FLASH MEMORY

Inventor: HA IM CHEOL (KR)

Applicant: HYNIX SEMICONDUCTOR INC (KR)

EC: G11C5/14P; G11C16/30

IPC: **G11C16/06; G11C5/14; G11C16/30** (+3)

Publication info: **KR2001060534** - 2001-07-07

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RESULT LIST

Approximately **473** results found in the Worldwide database for:
memory in the title AND **pointers** in the title or abstract
(Results are sorted by date of upload in database)

- 1 BUFFER MANAGEMENT SYSTEM/METHOD USING PRE-POINTER FIFO MEMORY HAVING POINTERS ABOUT EMPTY BUFFERS**
Inventor: WEVER TOMAS P (US) Applicant: SUN MICROSYSTEMS INC (US)
EC: G06F5/06P IPC: **G06F13/12; G06F5/06; G06F13/12** (+2)
Publication info: **KR2000006575** - 2000-01-25
- 2 Virtual storage device that uses volatile memory**
Inventor: MORESHET HANA (US); KOPYLOVITZ HAIM (US); (+3) Applicant: EMC CORP (US)
EC: IPC: **G06F13/10; G06F17/30; G06F3/06** (+5)
Publication info: **US7113945** - 2006-09-26
- 3 A lock-free memory allocator with delayed coalescing**
Inventor: GRANT CALUM A (GB) Applicant:
EC: IPC: **G06F12/00; G06F12/00**
Publication info: **US2006190697** - 2006-08-24
- 4 Self-correcting memory system**
Inventor: ANDERSEN ERIK (US) Applicant: BROADCOM CORP
EC: IPC: **H04L12/56; H04L12/28; H04L12/56** (+1)
Publication info: **US2006187941** - 2006-08-24
- 5 Memory device for use in high-speed block pipelined reed-solomon decoder, method of accessing the memory device, and reed-solomon decoder having the memory device**
Inventor: KWON HYUNG-JOON (KR); BAE IL-MAN (KR) Applicant:
EC: IPC: **H03M13/00; H03M13/00**
Publication info: **US2006184863** - 2006-08-17
- 6 METHOD AND APPARATUS TO SUPPORT MULTIPLE MEMORY BANKS WITH A MEMORY BLOCK**
Inventor: JAIN SANJEEV (US); WOLRICH GILBERT (US); (+2) Applicant: INTEL CORP (US); JAIN SANJEEV (US); (+3)
EC: G06F13/16D2 IPC: **G06F13/16; G06F13/16**
Publication info: **WO2006069126** - 2006-06-29
- 7 Sharing a block of memory between processes on a portable electronic device**
Inventor: CLARK WILLIAM ALLAN (GB) Applicant: SENDO INT LTD (HK)
EC: IPC: **G06F12/10; G06F9/46; G06F9/46** (+1)
Publication info: **GB2420642** - 2006-05-31
- 8 METHOD OF USING FLASH MEMORY FOR STORING METERING DATA**
Inventor: SEAL BRIAN (US); NORROD ERIC (US); (+1) Applicant: ITRON ELECTRICITY METERING INC (US); SEAL BRIAN (US); (+2)
EC: G01D9/00S IPC: (IPC1-7): G08B23/00
Publication info: **WO2006036650** - 2006-04-06
- 9 METHOD FOR RELIABLE RECORDING OF POINTER FOR CIRCULAR MEMORY**
Inventor: VAJSS DITER (DE) Applicant:
EC: G06F5/10; G11C16/10E IPC: **G06F11/14; G06F12/16; G06F5/10** (+9)
Publication info: **RU2269814** - 2006-02-10
- 10 No English title available**

Inventor:

Applicant:

EC:

IPC: G11B9/00; G11C7/00; G11B9/00 (+3)

Publication Info: RU2259604 - 2005-08-27

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memory in the title AND **pointers** in the title or abstract
(Results are sorted by date of upload in database)

- 31 Method and apparatus for using dual free link lists to manage packet memory**
Inventor: SINHA AJAY K (US); MAHARANA SURYAKANT (IN) Applicant:
EC: IPC: **H04L12/56; H04L12/56**; (IPC1-7): H04L12/56
Publication info: **US2005141516** - 2005-06-30
- 32 Restricting access to memory in a multithreaded environment**
Inventor: ALVERSON GAIL A (US); CALLAHAN CHARLES DAVID (US); (+4) Applicant:
EC: G06F9/46R2 IPC: **G06F9/46; G06F9/46**; (IPC1-7): G06F9/00 (+1)
Publication info: **US2004098721** - 2004-05-20
- 33 Content-addressable memory**
Inventor: MATSUMOTO KIMIHITO (JP); IINO TAKASHI (JP); (+3) Applicant:
EC: G11C15/00 IPC: **G06F12/00; G11C15/00; G11C15/04** (+3)
Publication info: **US2004085834** - 2004-05-06
- 34 PROCESSOR HAVING AN UNSHARED STACK MEMORY**
Inventor: TU LI-CHUN (TW); CHEN PING-SHENG (TW); (+2) Applicant:
EC: IPC: **G06F12/00; G06F12/00**; (IPC1-7): G06F12/00
Publication info: **US2004172514** - 2004-09-02
- 35 SEQUENTIAL ACCESS MEMORY**
Inventor: IMAMURA YUKINAGA; YAMANAKA KAZUYA; (+2) Applicant: MITSUBISHI ELECTRIC ENG; RENESAS TECH CORP
EC: IPC: **G11C11/401; G11C11/401**; (IPC1-7): G11C11/401
Publication info: **JP2004095175** - 2004-03-25
- 36 Systems and methods for transforming data in buffer memory without unnecessarily copying data to additional memory locations**
Inventor: BLASZCZAK MICHAEL A (US) Applicant: MICROSOFT CORP (US)
EC: IPC: **G06F15/16; G06F17/00; G06F15/16** (+2)
Publication info: **US2004186843** - 2004-09-23
- 37 Very high speed page operations in indirect accessed memory systems**
Inventor: FRANASZEK PETER A (US); SCHULZ CHARLES O (US); (+3) Applicant: IBM (US)
EC: G06F12/10D IPC: **G06F12/10; G06F12/10**; (IPC1-7): G06F12/08
Publication info: **US2004123069** - 2004-06-24
- 38 Multilevel segmented memory**
Inventor: RUDELIC JOHN C (US) Applicant:
EC: IPC: **G06F12/00; G06F12/00**; (IPC1-7): G06F12/00
Publication info: **US2005066130** - 2005-03-24
- 39 Memory wear leveling**
Inventor: VIHMALO JUKKA-PEKKA (FI); AHVENAINEN MARKO T (FI); (+1) Applicant: NOKIA CORP (US)
EC: IPC: **G06F12/16; G06F12/16**; (IPC1-7): G06F12/16
Publication info: **US2005055495** - 2005-03-10
- 40 Circuit design method to control access pointers of different memory**
Inventor: LIN CHE-SHENG (TW) Applicant:

EC: G06F17/50D

IPC: **G06F17/50; G06F17/50**; (IPC1-7): G06F17/50

Publication info: **US2005018491** - 2005-01-27

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41 Memory-based shuffle-exchange traceback for gigabit ethernet transceiver

Inventor: LUETKEMEYER CHRISTIAN (US)

Applicant:

EC: H03M13/39; H03M13/41; (+2)

IPC: **H03M13/39; H03M13/41; H04L1/00** (+3)

Publication info: **US2004054957** - 2004-03-18

42 Method and apparatus for providing a packet buffer random access memory

Inventor: JONES DAVID E (CA)

Applicant: MOSAID TECHNOLOGIES INC (CA)

EC: H04L12/56Q1

IPC: **H04L12/56; H04L12/56**; (IPC1-7): H04L12/54

Publication info: **US2004008714** - 2004-01-15

43 Method and apparatus for determining a status of an asynchronous memory

Inventor: LO JOHN (US)

Applicant:

EC:

IPC: **H02H3/05; H02H3/05**; (IPC1-7): H02H3/05

Publication info: **US2004255188** - 2004-12-16

44 Methods and systems of cache memory management and snapshot operations

Inventor: BURTON DAVID ALAN (US); OTTERNESS NOEL SIMEN (US)

Applicant:

EC: G06F12/08B12

IPC: **G06F12/08; G06F12/08**; (IPC1-7): G06F12/00

Publication info: **US2004230737** - 2004-11-18

45 Flash memory-disk watch

Inventor: CHUNG YI SHING (HK)

Applicant:

EC: G04G1/02; G11B33/02; (+1)

IPC: **G04G1/02; G11B33/02; G11B33/12** (+5)

Publication info: **US2004001392** - 2004-01-01

46 VLAN TABLE MANAGEMENT FOR MEMORY EFFICIENT LOOKUPS AND INSERTS IN HARDWARE-BASED PACKET SWITCHES

Inventor: NAVADA MURALEEDHARA; KURUPATI SREENATH

Applicant: INTEL CORP (US)

EC: H04L12/46V

IPC: **H04L12/46; H04L12/46**; (IPC1-7): H04L12/56 (+1)

Publication info: **WO03081857** - 2003-10-02

47 CACHE MEMORY

Inventor: GOTO SEIJI (JP)

Applicant: FUJITSU LTD (JP); GOTO SEIJI (JP)

EC: G06F12/08B16V

IPC: **G06F12/08; G06F12/08**; (IPC1-7): G06F12/08

Publication info: **WO2004077299** - 2004-09-10

48 Conservative garbage collectors that can be used with general memory allocators

Inventor: RODRIGUEZ-RIVERA GUSTAVO (US); SPERTUS MICHAEL P (US); (+1)

Applicant:

EC: G06F12/02D2G

IPC: **G06F12/02; G06F12/02**; (IPC1-7): G06F12/00

Publication info: **US2004139272** - 2004-07-15

49 BUFFER MEMORY DEVICE AND BUFFER MEMORY CONTROL METHOD

Inventor: YOTSUMOTO KAZUAKI

Applicant: FUJITSU LTD

EC:

IPC: **G06F12/06; G06F12/02; G06F13/38** (+9)

Publication info: **JP2004240711** - 2004-08-26

50 MEMORY MAPPING DEVICE UTILIZING SECTOR POINTERS

Inventor: CERNEA RAUL-ADRIAN

Applicant: SANDISK CORP (US)

EC: G06F3/06E; G06F12/02D2E2; (+1)

IPC: *G11C16/02; G06F3/06; G06F12/00* (+8)

Publication info: **WO03067437** - 2003-08-14

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IEE JNL IEE Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IEE CNF IEE Conference Proceeding

IEEE STD IEEE Standard

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Grant, D.; Hayward, V.;
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Volume 17, Issue 3, June 1997 Page(s):80 - 88
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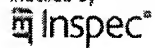
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#15 (((memory<in>metadata) <and> (control<in>metadata))
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#16 (((memory<in>metadata) <and> (control<in>metadata))
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pyr <= 2002)

